# Supplemental Association Data Sheet

## **Application Information**

Application Type:: Regular

Subject Matter:: Utility

Title:: Semiconductor Integrated Circuit Device

Attorney Docket Number:: 501.36127CC3

Request for Early Publication?:: No

Request for Non-Publication?:: No

Total Drawing Sheets:: 27

Small Entity:: No

**Applicant Information** 

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## Foreign Priority Information

Country:	Application Number:	Filing Date:	Priority Claimed:
Japan	9-81013	March 31, 1997	Yes
Japan	10-33388	February 16, 1998	Yes

### Assignee Information

Assignee name: Renesas Technology Corp.

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